

FIG. 1

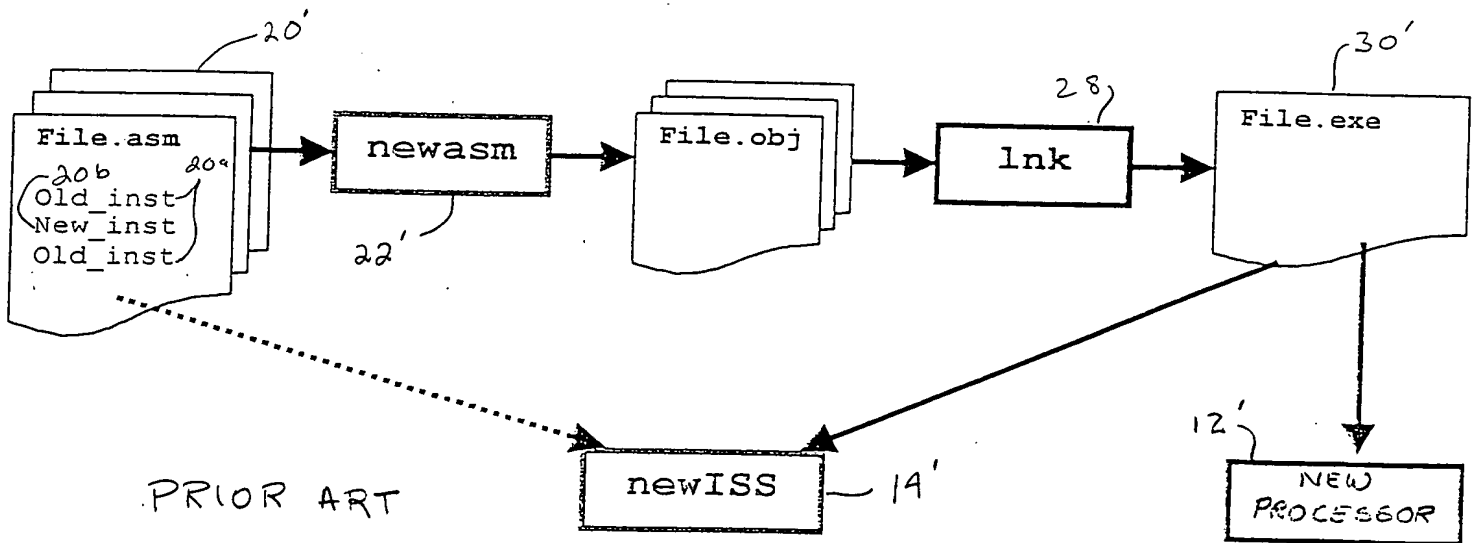


FIG. 2

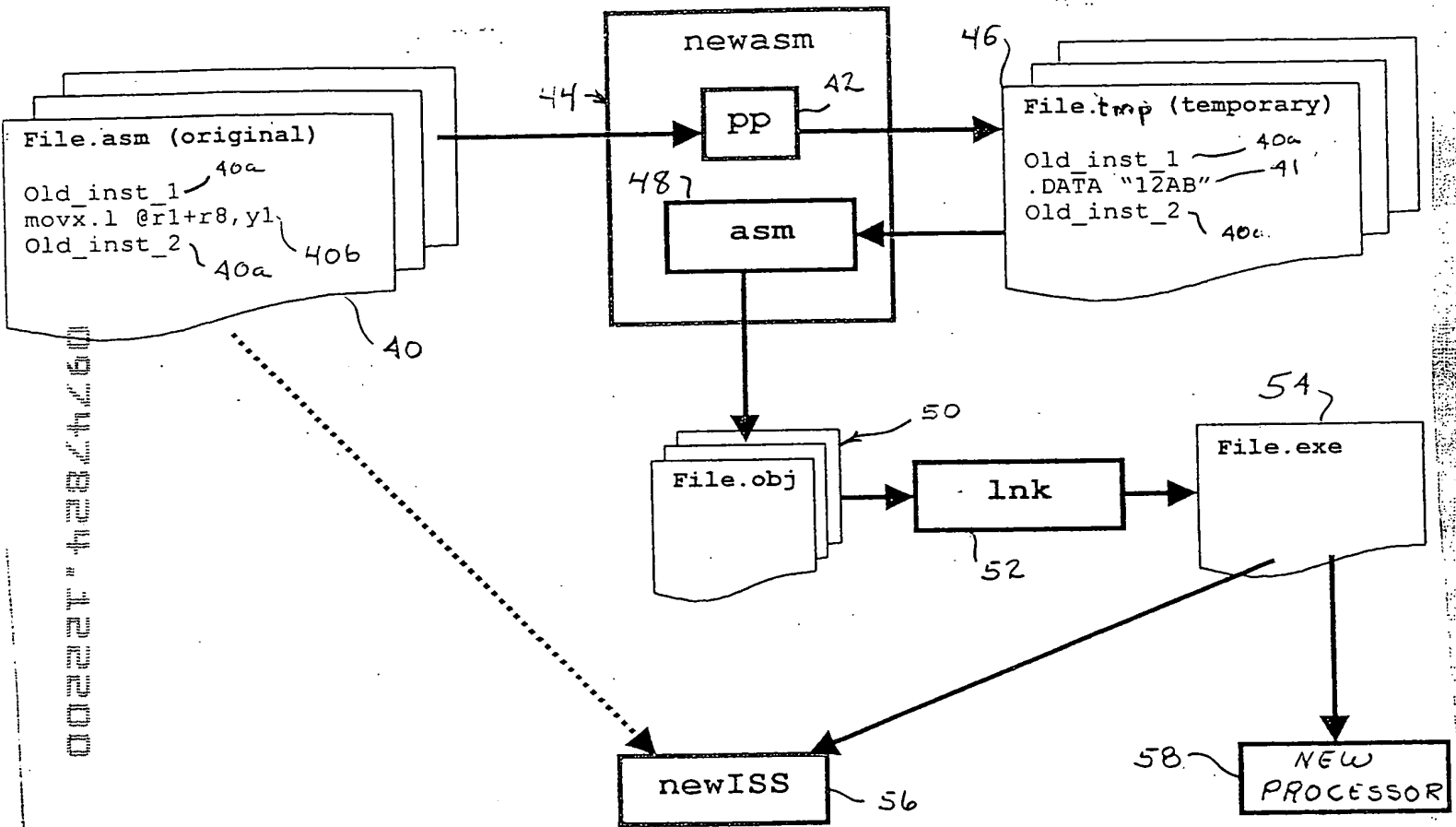


FIG-3

# PREPROCESSOR OPERATION

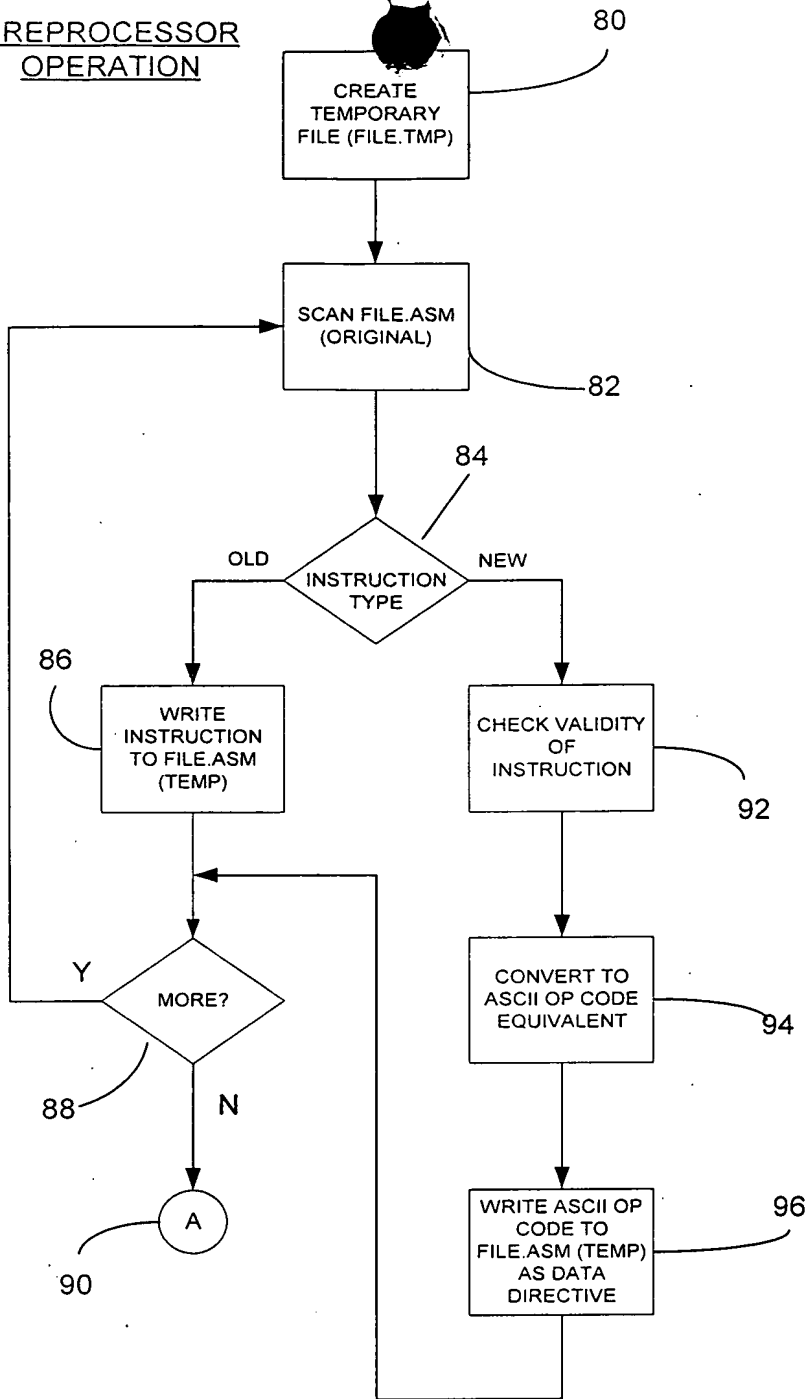


FIG.\_4B

# NEWASM

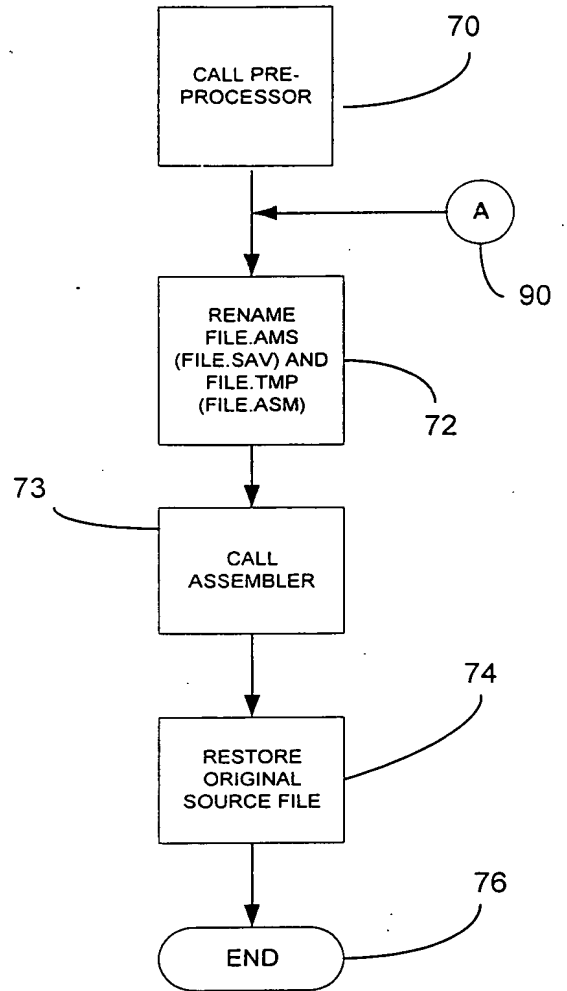


FIG.\_4A